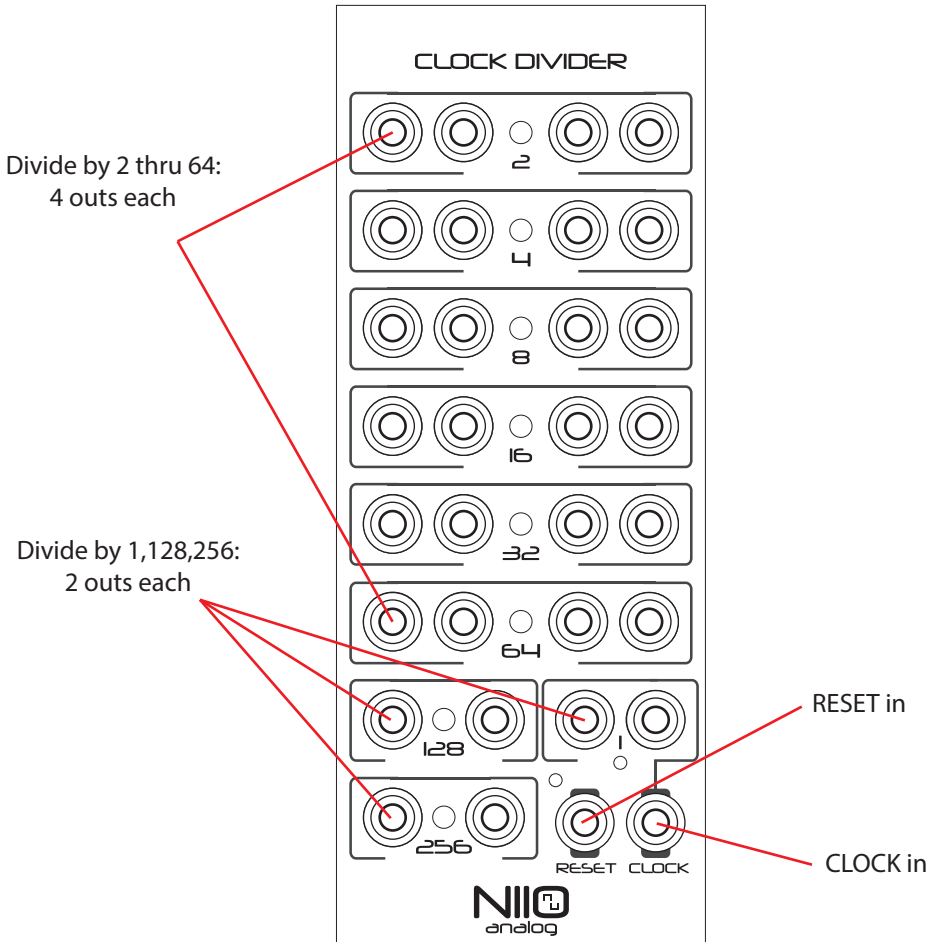# CLOCK DIVIDER

CLOCK DIVIDER is used to divide a clock signal down by divide by 1 thru divide by 256 with four outputs for 2-64 divisions, and two outs for 1, 128, and 256.

CLOCK DIVIDER uses a 4000 series CMOS ripple counter for stable analog divisions. Each division has a red LED that illuminates while the gate output for that division is high.
A yellow RESET LED illuminates with a reset gate.

Divide by 2 thru 64:
4 outs each

Divide by 1,128,256:
2 outs each

RESET in

CLOCK in

width: 10hp   depth: 33mm   6ma +12v
niioanalog.com